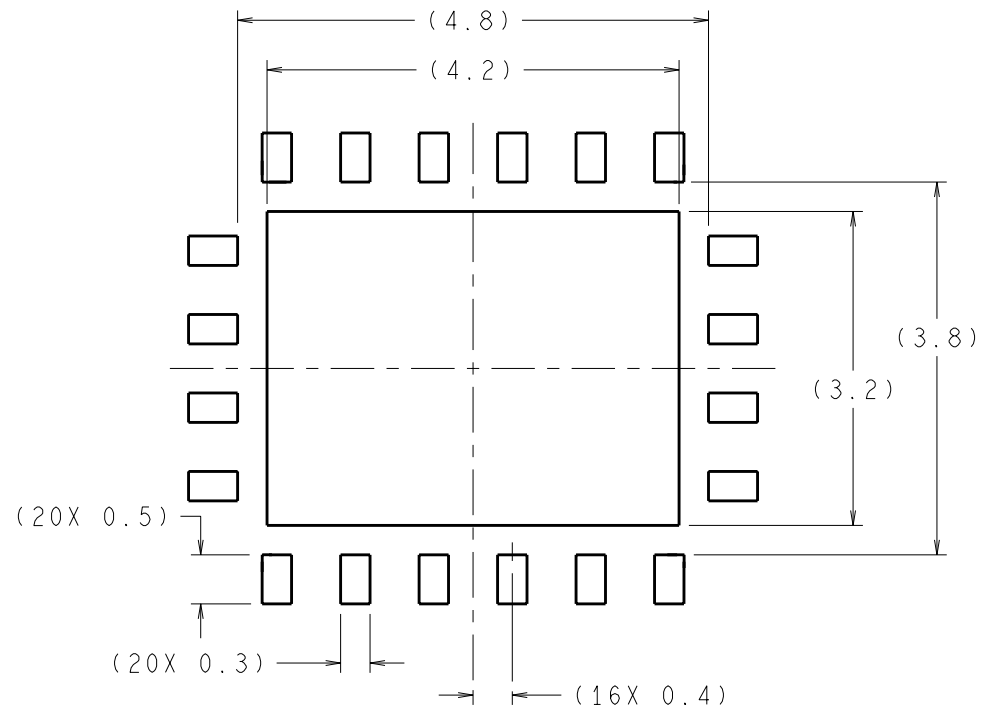
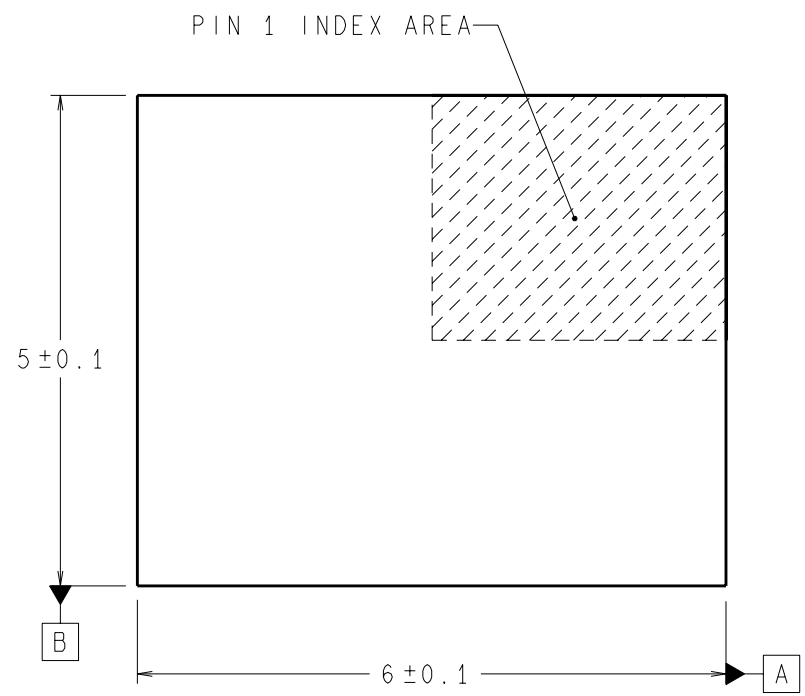


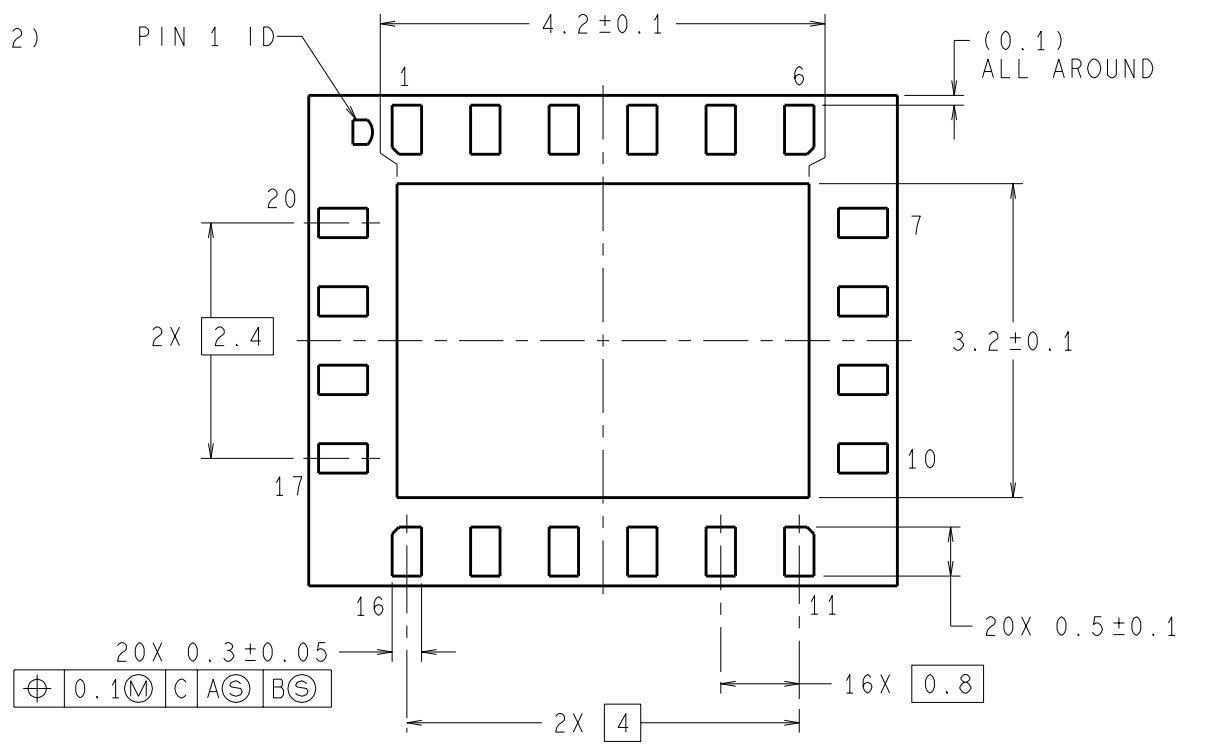
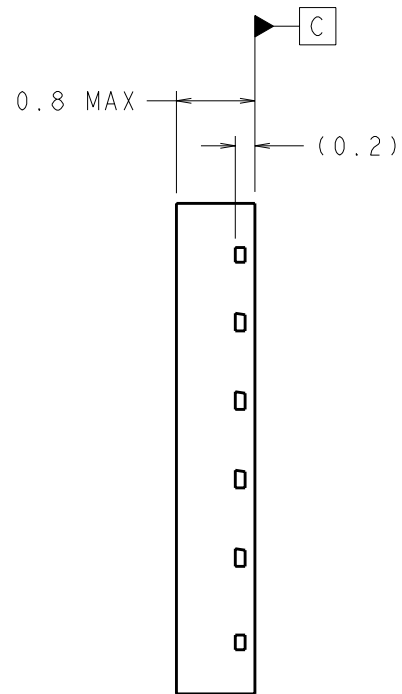
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
A	RELEASE TO DOCUMENT CONTROL	12577	01/22/2001	AS/TL/SN



RECOMMENDED LAND PATTERN
1:1 RATIO WITH PKG SOLDER PADS



DIMENSIONS ARE IN MILLIMETERS



NOTES: UNLESS OTHERWISE SPECIFIED

- STANDARD LEAD FINISH TO BE 5.08 MICROMETERS MINIMUM LEAD/TIN (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF JANUARY 2001.

APPROVALS	DATE	National Semiconductor			
DRAWN AFZMAM S.	01/22/2001	2900 Semiconductor Dr., Santa Clara, CA 95052-8090			
DFTG. CHK. THANH LEQUANG	01/22/2001	LLP, PLASTIC, QUAD, 6 X 5 X 0.8mm BODY, 20 LD, 0.8mm PITCH			
ENGR. CHK. N. SANTHIRAN	01/22/2001				
		SCALE N/A	SIZE C	DRAWING NUMBER (SC)MKT-LQC20A	REV A
		DO NOT SCALE DRAWING		SHEET 1 of 1	